

Orange-5 & NEC V850 series V3.6

<http://www.cnc-lab.com>

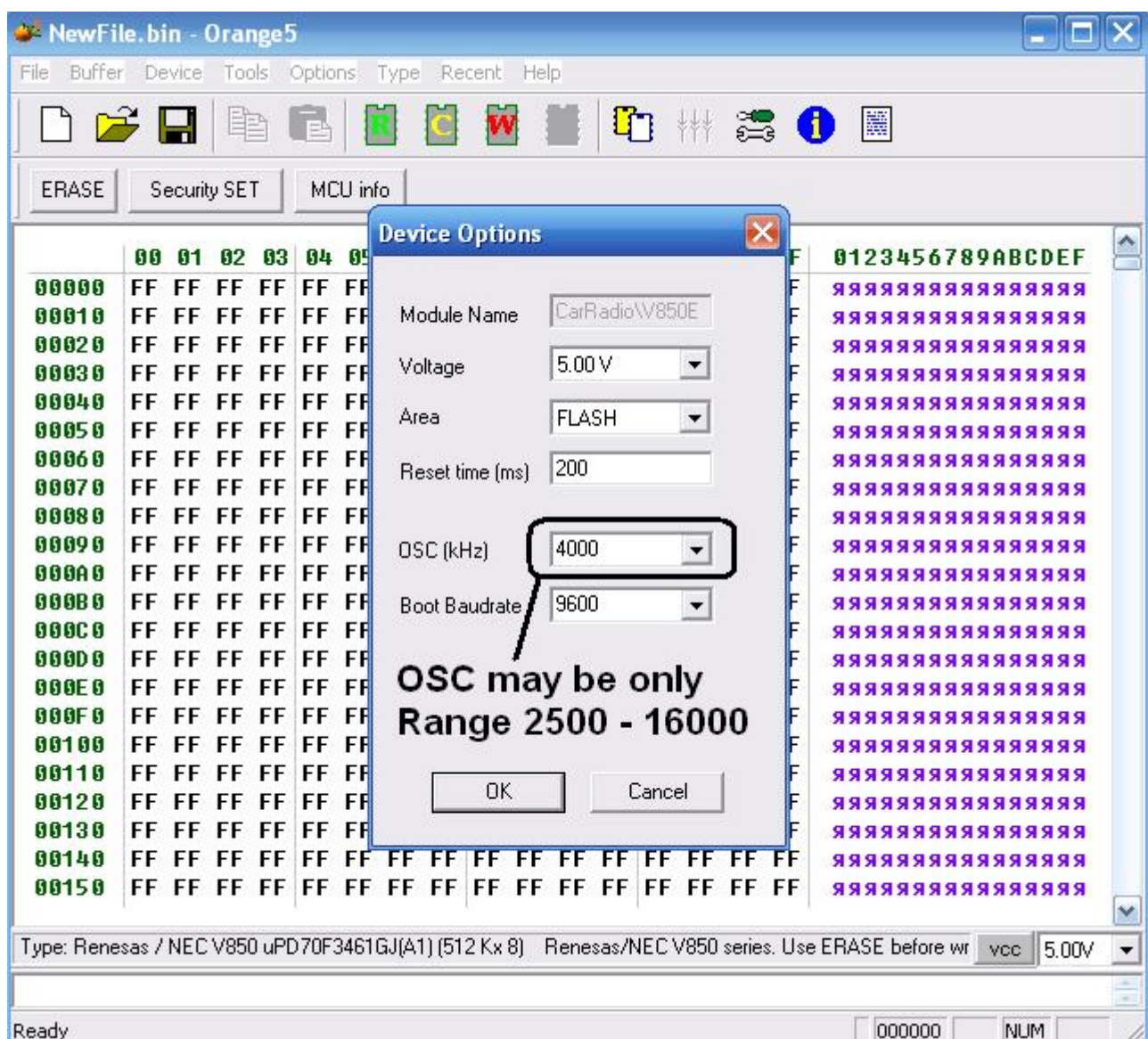
<http://pavel-pervomaysk.com>

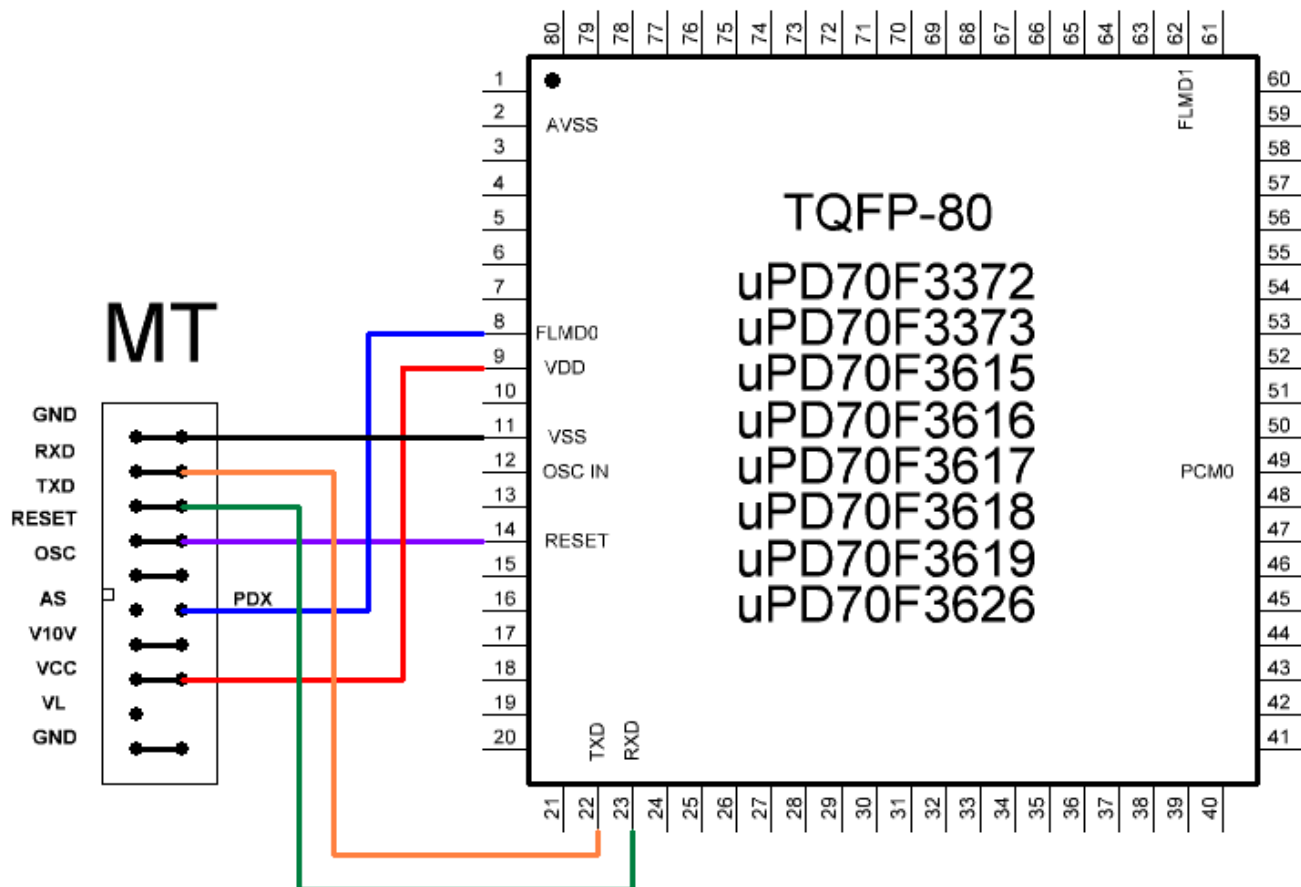
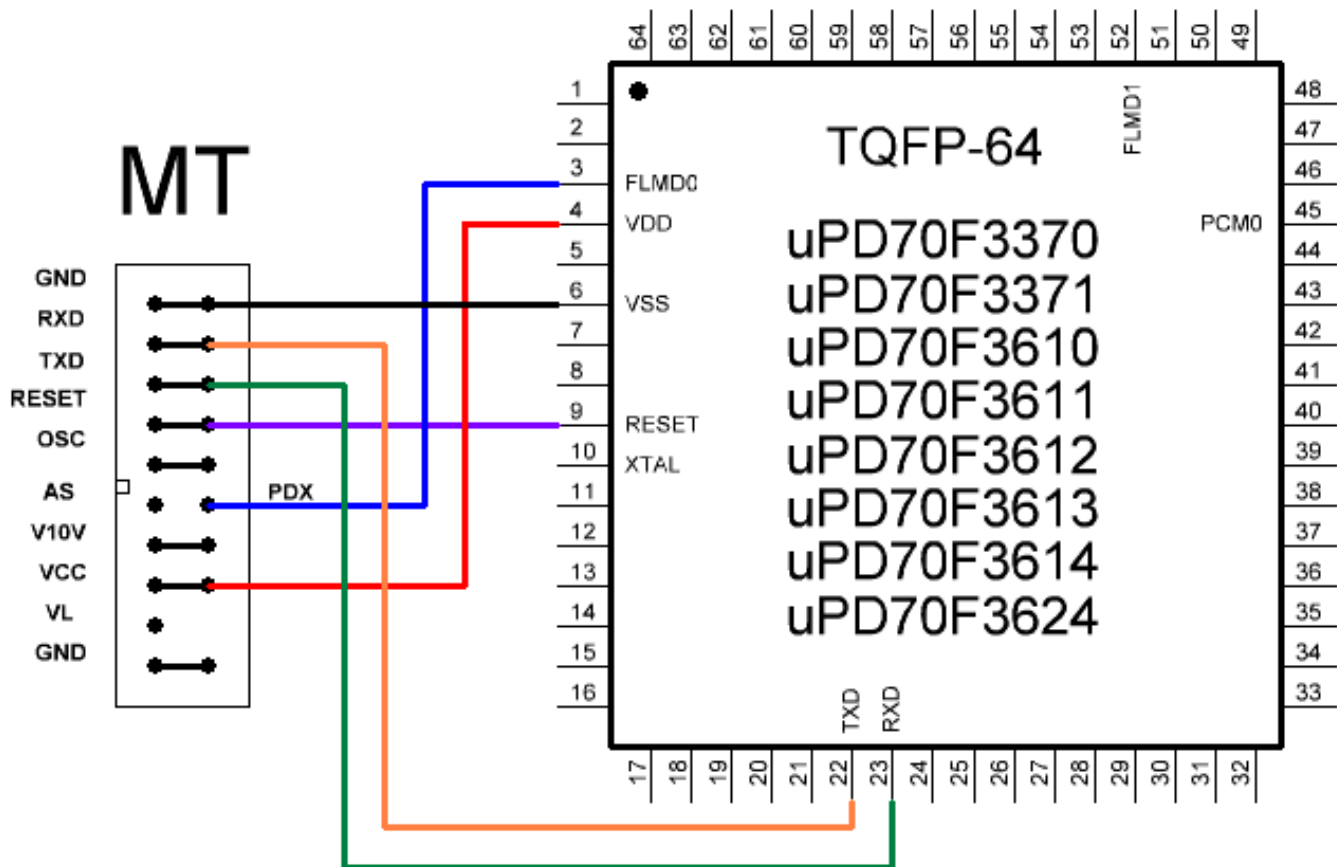
Connecting to the programming of microcontrollers using the examples presented in this document.

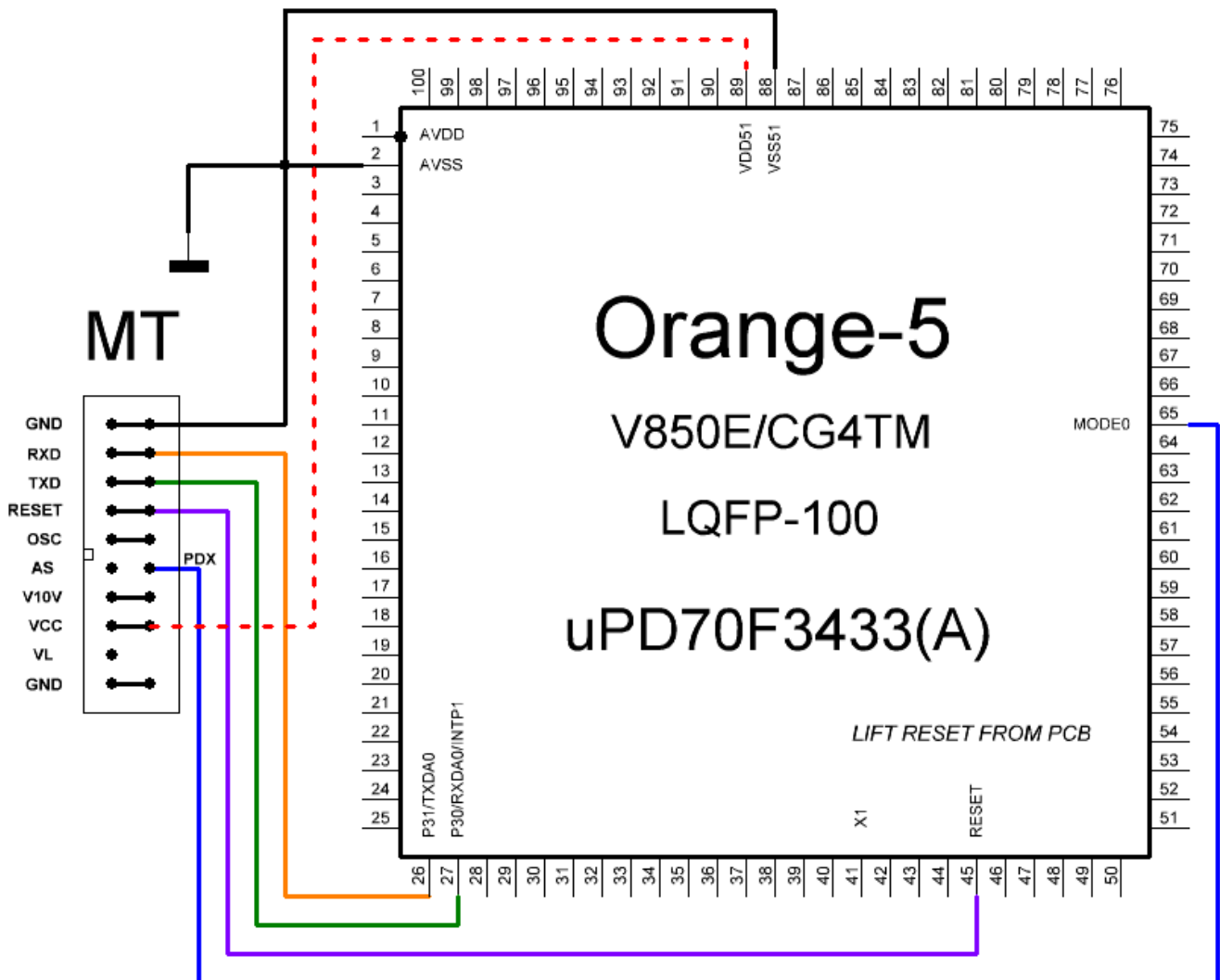
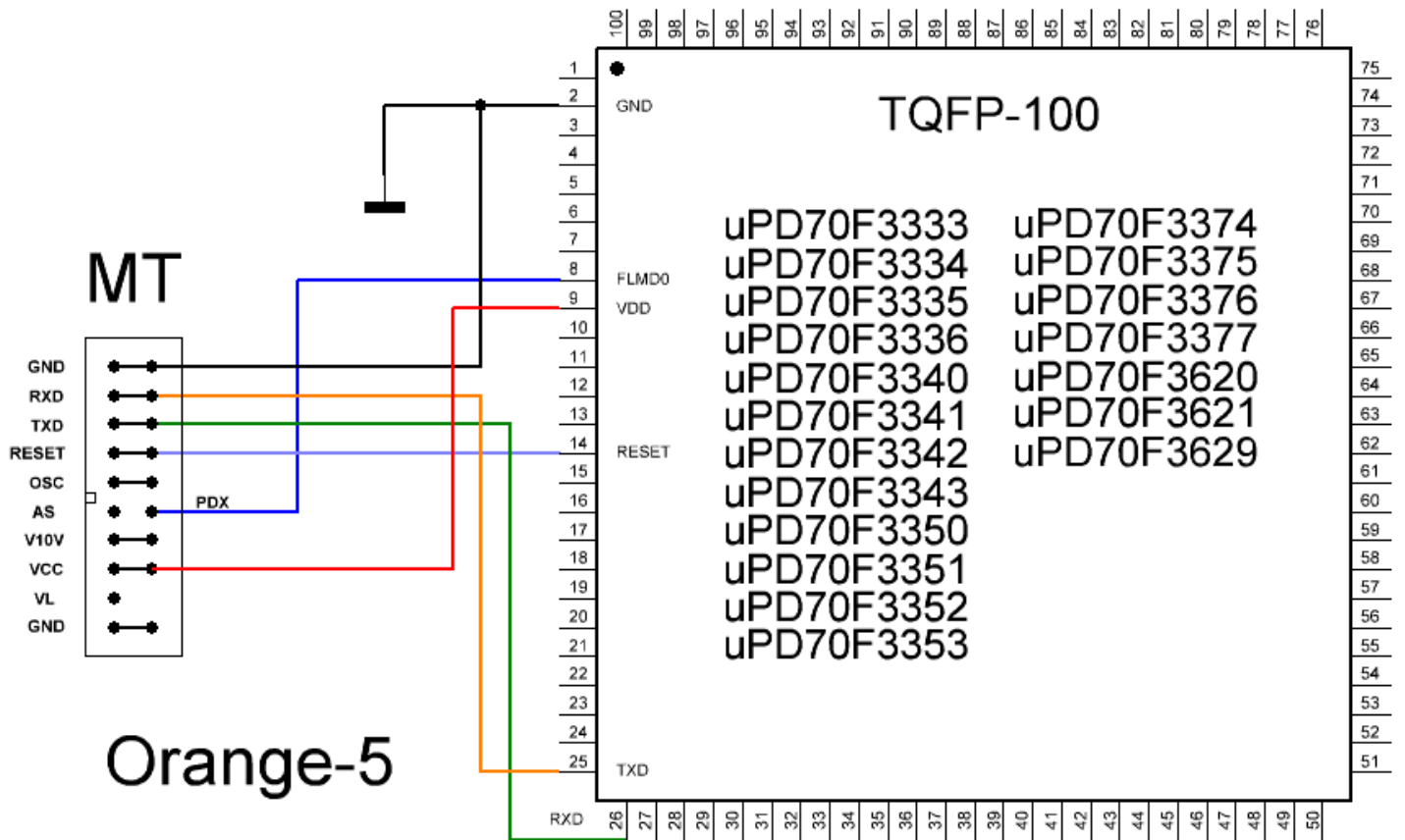
Read/Write **DATAFLASH** AREA without erasing all chip added in module V850ES.hpx V3.4.

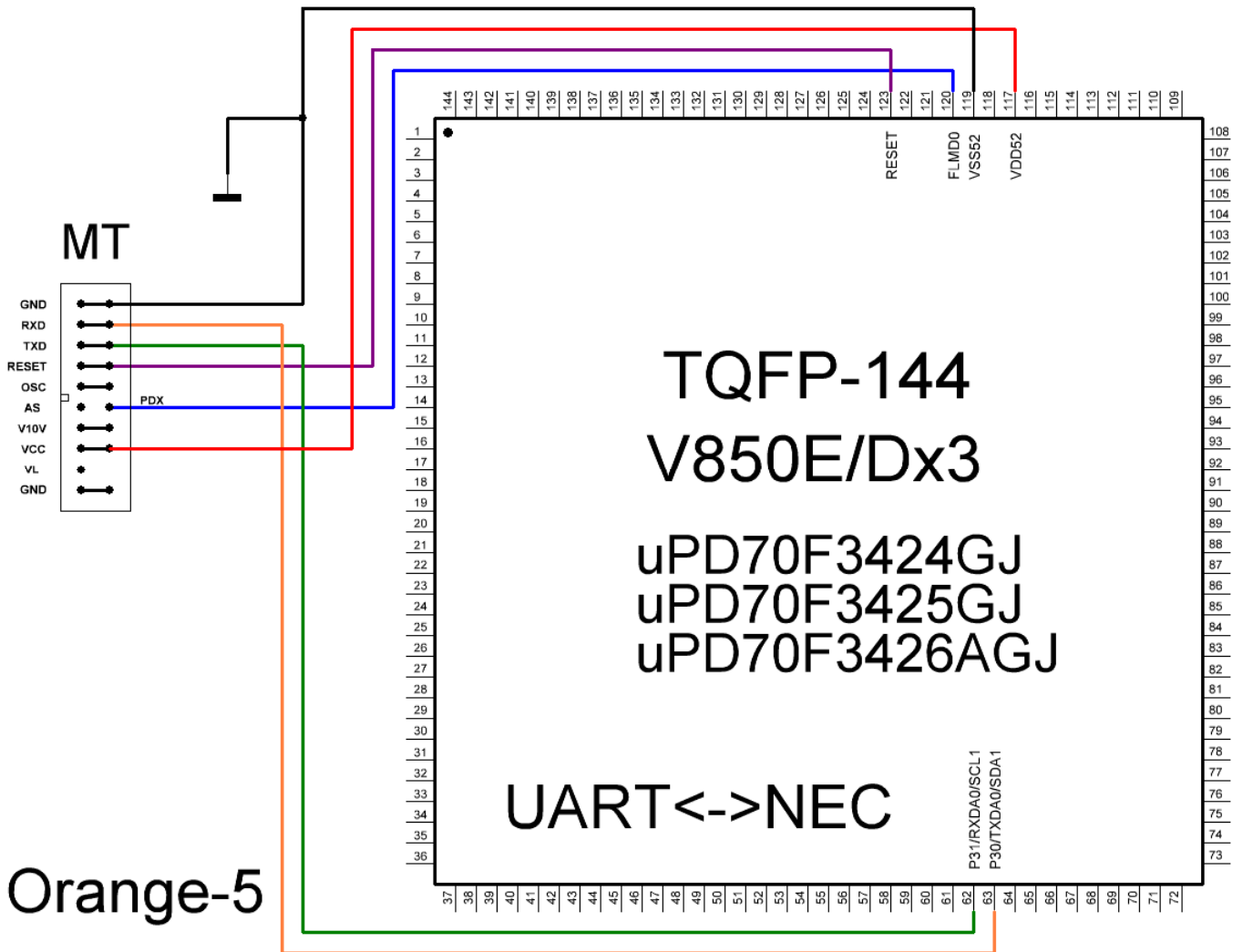
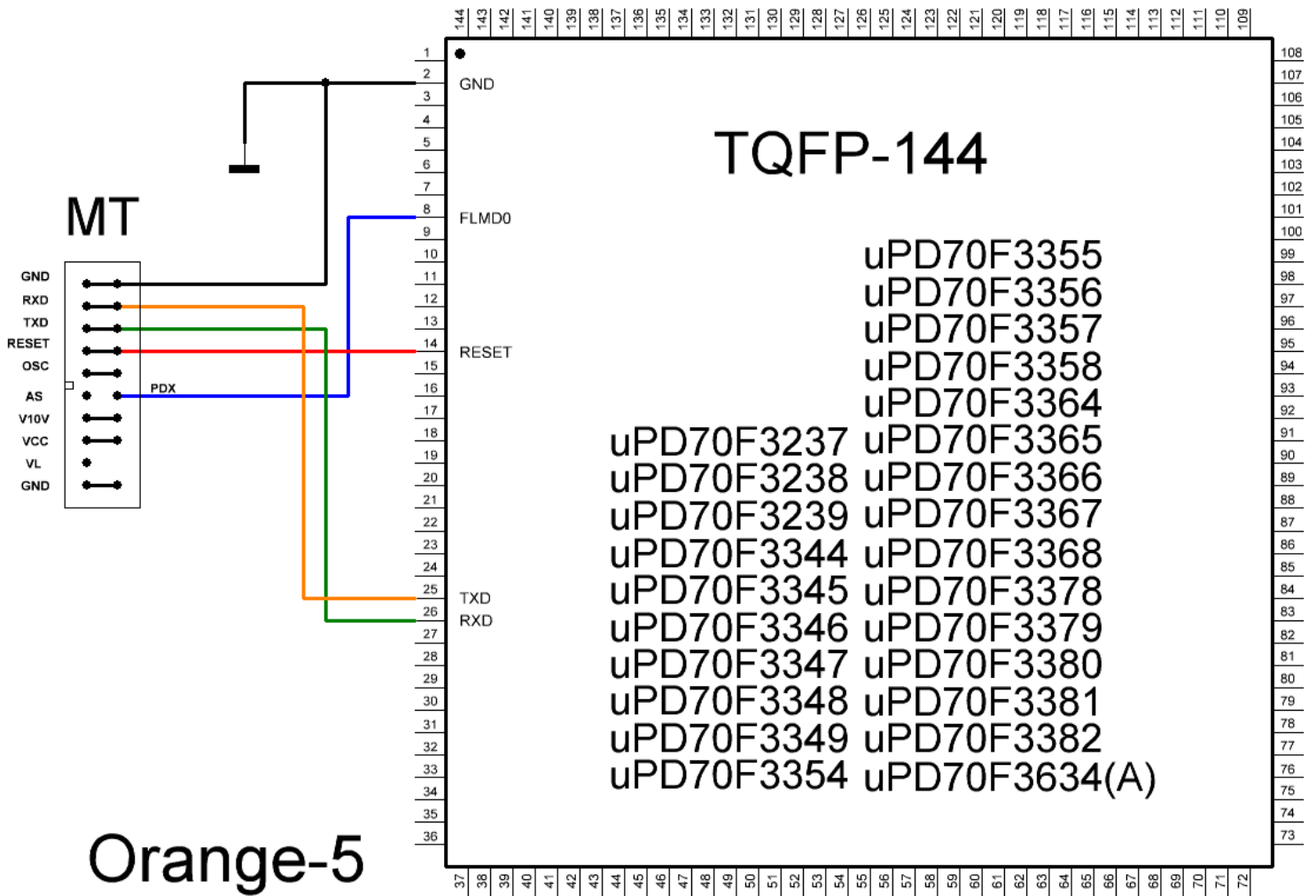
- 1) Check the power supply (3.25 or 5.0 volts) and select right
- 2) Check oscillator value ! Default value 4.000 MHz (4000)
- 3) Check the logic levels of the pins **TXD**, **RXD**, **RESET**, **FLMD0**
- 4) In some devices you should lift programming pins from PCB

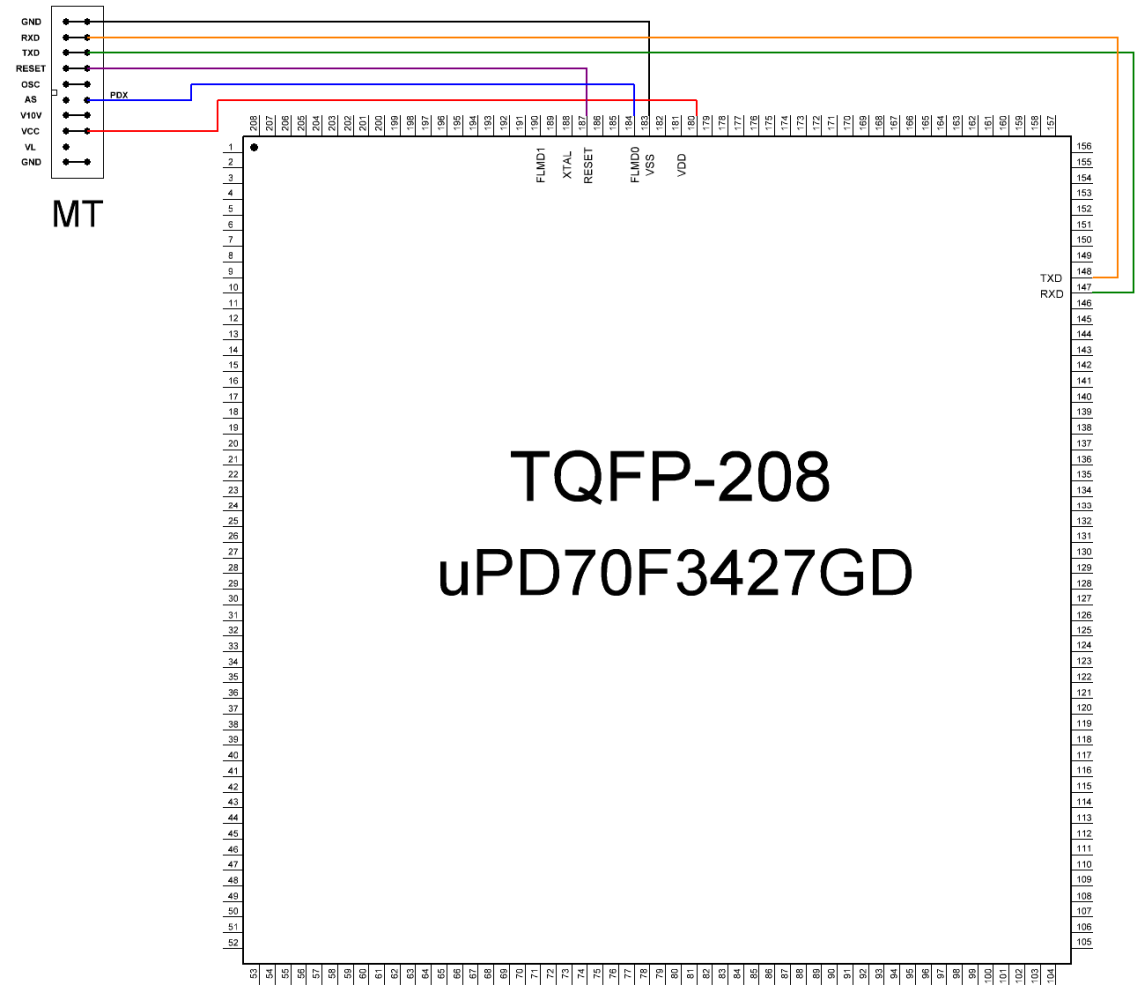
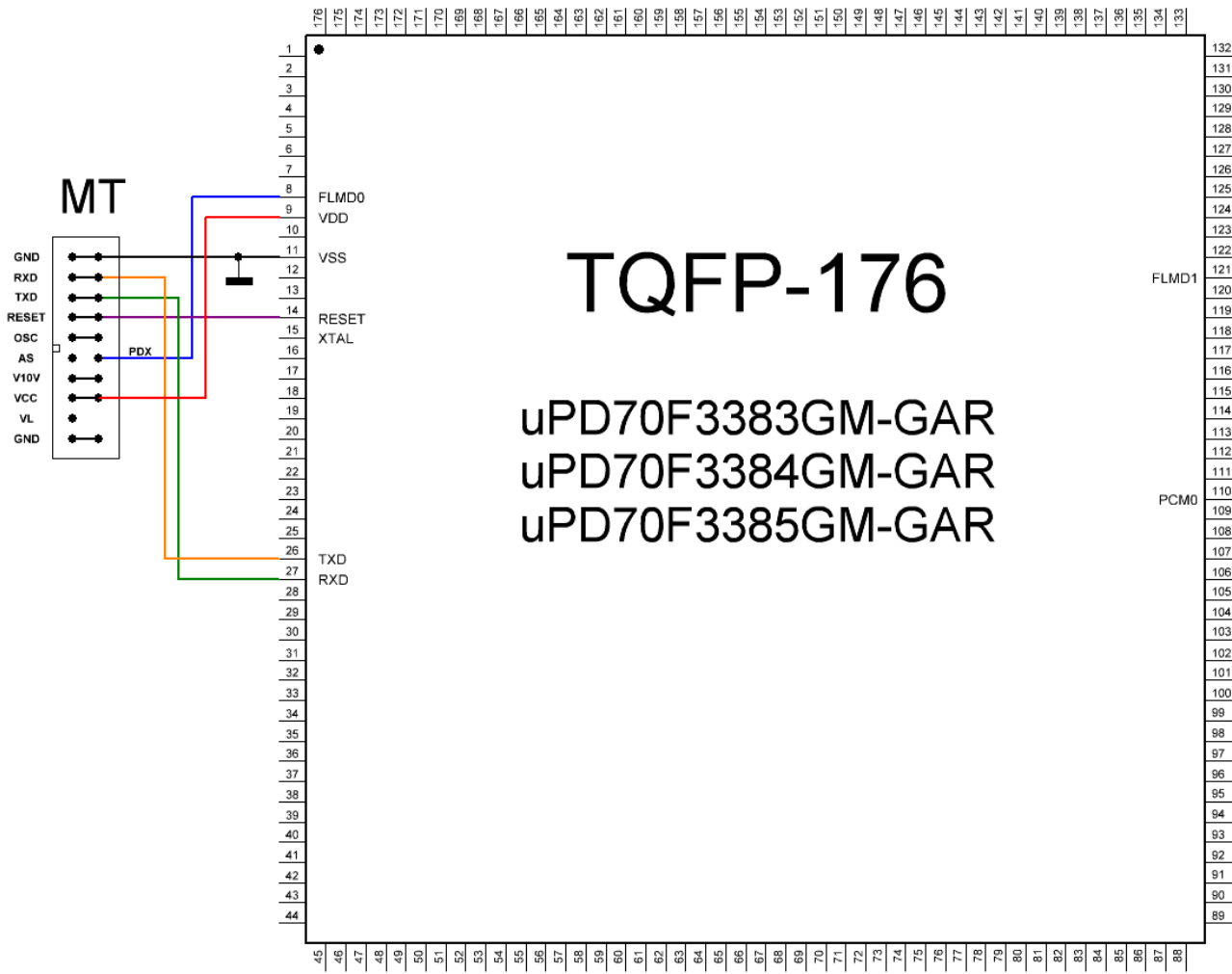
Added new chips.

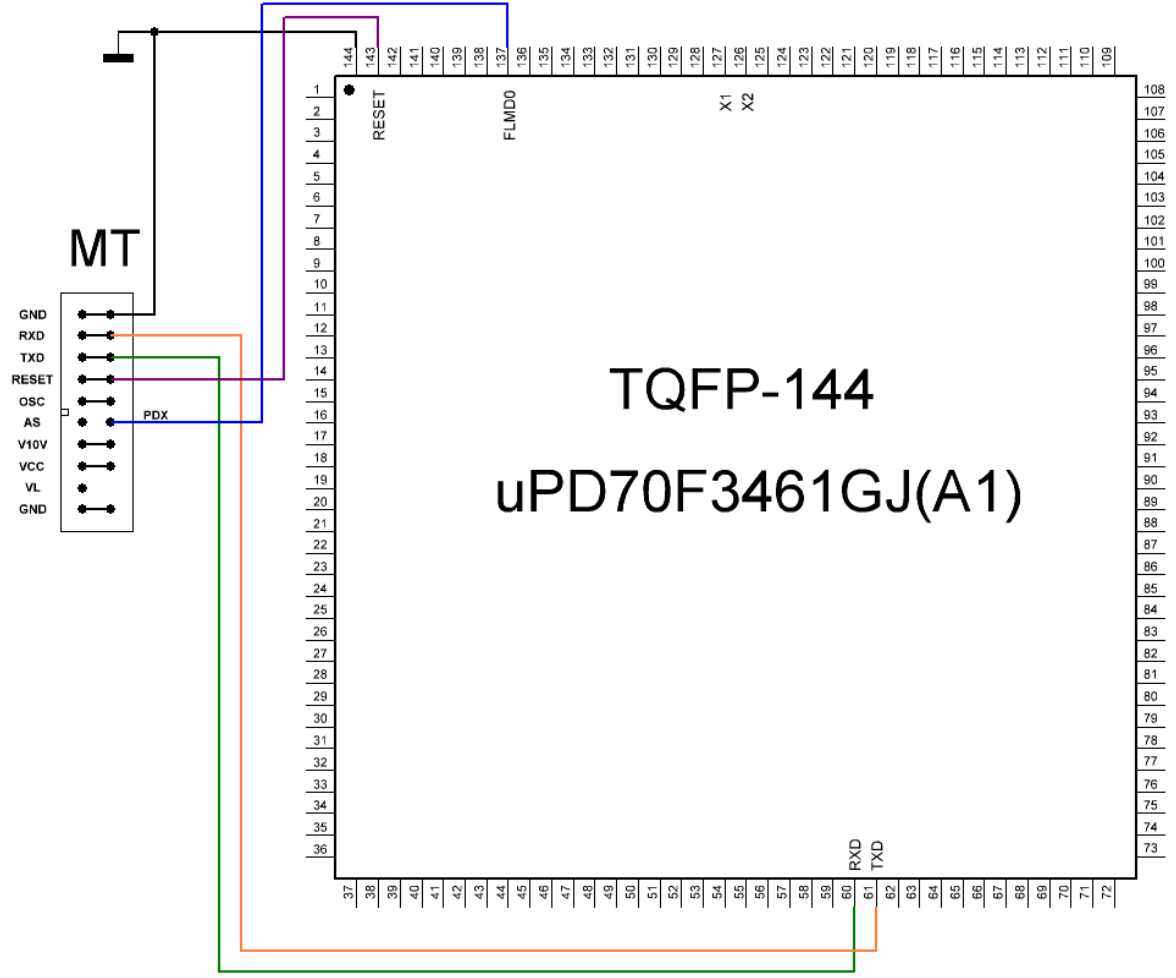


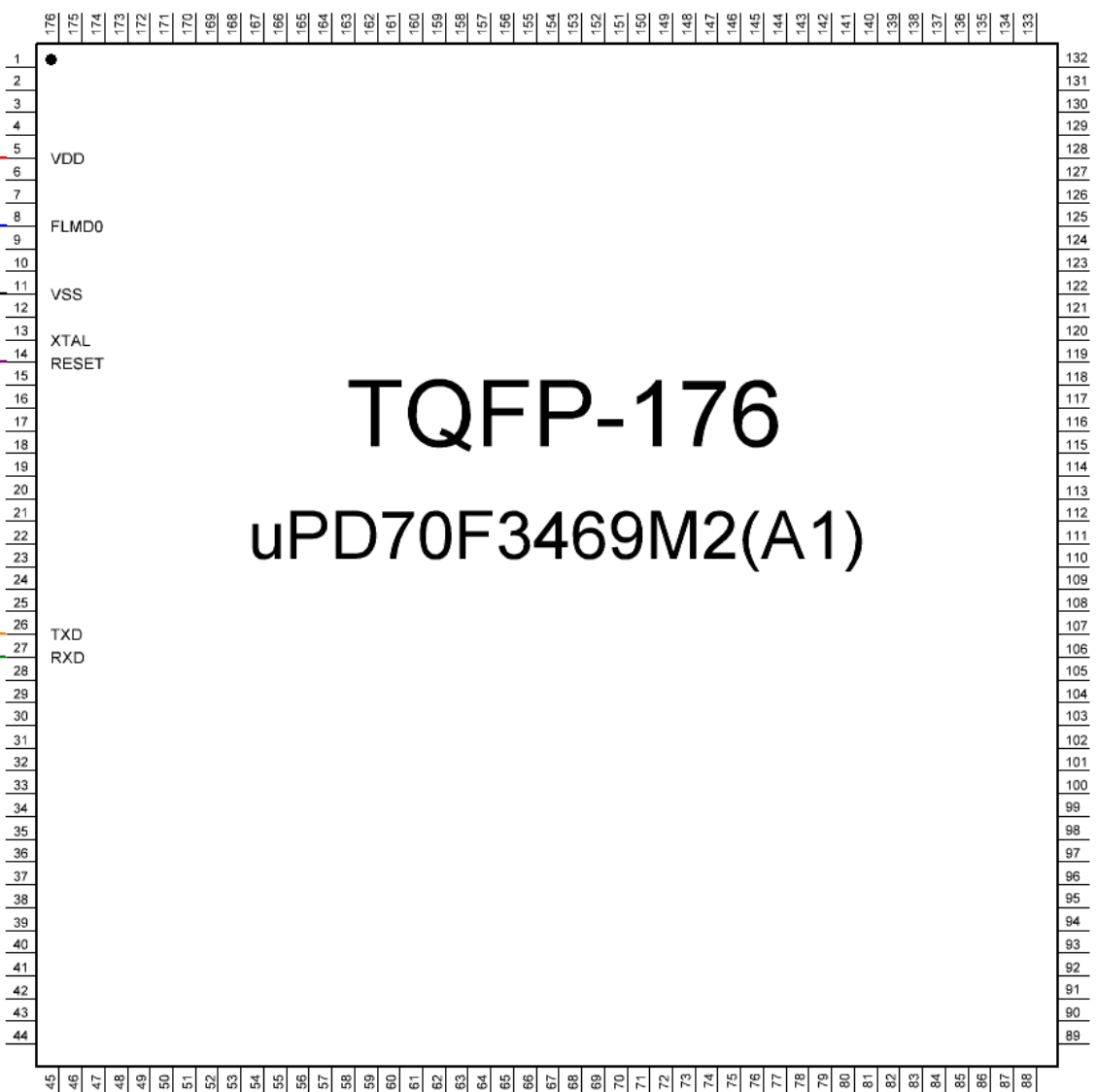
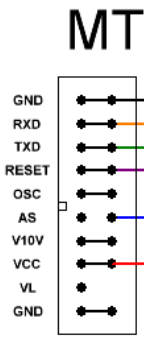






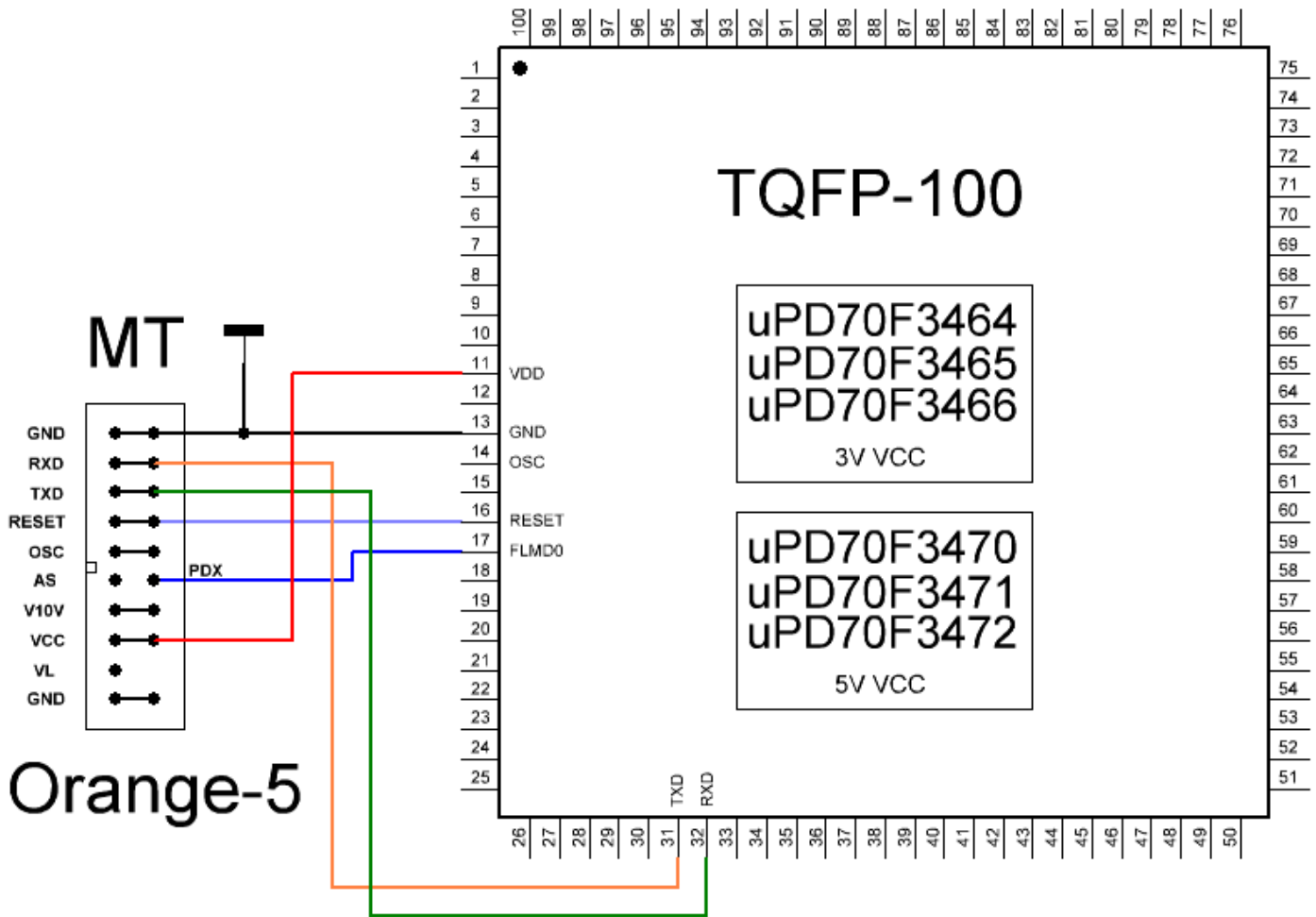






TQFP-176

uPD70F3469M2(A1)



FAQ:

1) Why always I see the error : “MCU not answer”?

Look at the page 1, after read more.

This is because the CPU does not respond.

For responding MCU in boot you should provide this modes.

Power supply, range 3.25 or 5.0 volts.

Logic levels on all boot pins.

There are four pins:

FLMD0 – Logic 1; RESET – Logic 1; RXD – Logic 1; TXD – Logic 1;

Often interference on TXD, RXD pins provided by LIN, k-line controller

On RESET pin often disturb logic level MCU RESET supervisor.

Logic 1 for 3.25 volts VCC = 3.25V

Logic 1 for 5.0 volts VCC = 5.0V

Logic 0 for both modes = 0 V, Gnd

Please use oscilloscope for checking logic levels on Boot pins.

Best regards by pavel-pervomaysk.